

ABSTRACT

The invention provides a method of controlling a jitter buffer, which sets a packet delete area, a packet add area, and a clock control area inside a FIFO forming the jitter buffer. The method controls to delete packets when the stored packet quantity is within the packet delete area, controls to add packets when the stored packet quantity is within the packet add area, and controls to raise or lower the clock frequency for reading the packets when the stored packet quantity is within the clock control area, in which the clock control area is set between the packet add area and the packet delete area.